

**SYSTEM AND METHOD FOR ACCELERATED INFORMATION
HANDLING SYSTEM MEMORY TESTING**

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ABSTRACT OF THE DISCLOSURE

Memory testing at system startup, such as boot POST, of an information
10 handling system is accelerated by adjusting memory testing routines to use
instructions that take advantage of optimizations made to information handling system
and CPU architectures. For instance, memory test iterations in one Mbyte portions
using 128-bit SIMD registers, 64-bit MMX registers, ADD and SUB instructions, the
MOVNTDQ instruction, and relying on an initial setting of the gate A20 and
15 protected mode result in a substantially accelerated memory test.